

COURSE CONTENT STATEMENT:

TITLE: Year 12 Electronics

LEVEL: 2

INTRODUCTION: The course aims to empower students with the ability to critically analyse situations and approach new situations with confidence, while gaining knowledge of the fundamental concepts and principles of electronics. You are encouraged to use scientific methods to challenge current thinking and provide input and feedback on course delivery.



TERM DATES FOR THE YEAR:

Term 1: 30 January – 5 April

Term 2: 23 April – 29 June

Term 3: 16 July – 28 September

Term 4: 15 October – 13 December

External examinations: 10 November – 5 December 2012

STANDARDS TO BE DELIVERED:

The course is based on the 3 Unit Standards listed below and 3 Achievement Standards listed below:

| | |
|-----------|----------------------------------------------------------------------------------------------------|
| US 18240 | Demonstrate knowledge of basic electronic components |
| US 18242 | Construct a simple printed circuit |
| US 18243 | Construct simple electronic products from supplied schematics |
| AS DT2.47 | Demonstrate understanding of advanced concepts used in the construction of electronic environments |
| AS DT2.48 | Implement advanced interfacing procedures in a specified electronic environment |
| AS DT2.49 | Implement advanced techniques in constructing a specified advanced electronic and embedded system |

REASSESSMENT POLICY for INTERNAL ASSESSMENT:

Repeat assessment:

The opportunity to have one repeat of an assessment will differ from standard to standard. The standard/s that will have a repeat assessment after further learning has taken place are:

18240 Demonstrate knowledge of basic electronic components

All students who gained 'Not Achieved' and students with a valid reason for missing the first assessment are expected to sit the repeat assessment. The highest grade will be recorded.

Students will not be allowed a further assessment opportunity where they have chosen not to take the first opportunity. Please refer to your NCEA Student Handbook for the procedure to follow when you have a valid reason for missing assessment.

The following standard/s will not be reassessed before the following year:

| | |
|----------|---------------------------------------------------------------|
| US 18242 | Construct a simple printed circuit |
| US 18243 | Construct simple electronic products from supplied schematics |

| | |
|-----------|----------------------------------------------------------------------------------------------------|
| AS DT2.47 | Demonstrate understanding of advanced concepts used in the construction of electronic environments |
| AS DT2.48 | Implement advanced interfacing procedures in a specified electronic environment |
| AS DT2.49 | Implement advanced techniques in constructing a specified advanced electronic and embedded system |

Resubmission:

A resubmission opportunity may be offered where your teacher judges that a mistake has been made, which you should be capable of discovering and correcting yourself.

REQUIREMENTS FOR ENTRY TO NEXT LEVEL:

Level 3: 14 Credits in 12ELC, OR 14 Credits in 12PHY including P2.6 and 15 Credits in 12MAT including M2.1

CAREERS RELATED TO THIS COURSE:

The course can lead directly to employment in one of the many branches of the electronics industry or form a useful background to the Year 13 Physics course, various Technical Institute courses, e.g. NZCS and NZCE and University electronics and engineering courses.

SUBJECT SPECIFIC INFORMATION:

For practical assessments, an authenticity statement must be signed to verify that all work is your own.